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EP673131 Tokyo european software patent - Hierarchical ofdm system ...
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> (WO/2002/009298) WIRELESS INTERACTIVE SYSTEM AND METHOD
> Similarly, a small convolutional interleaver may be used. ... 12 details the downstream OFDM carriers allocation in the frequency domain, with frequency ...
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... an address generator for generating a write address signal and a read a convolutional interleaver 832, convolutional encoding and puncturing unit ...
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An OFDM receiver obtains, for each symbol, a composite channel ...
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1 Interleaver for mapping symbols on the carriers of an OFDM system

Inventor: PERON JEAN-LUC (GB)
Applicant: SONY UK LTD (GB)
EC: H04L27/26M5
IPC: *H03M13/27; H04L27/26; H04N5/44* (+8)
Publication info: EP1463255 - 2004-09-29

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1 Interleaver for mapping symbols on the carriers of an OFDM system

Inventor: PERON JEAN-LUC (FR)
Applicant: SONY UK LTD (GB)
ECO: H03M13/27; H04L27/26M5; (+1)
IPC: *H04L27/26; H03M13/27; H04N5/44* (+5)
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Wireless Communications, IEEE Transactions on
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2. Group OFDM technique for beyond 3G
Author, F.A.; Author, S.B., Jr.; Author, T.C.;
Personal, Indoor and Mobile Radio Communications, 2005. PIMRC 2005. IEE Symposium on
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Yiyan Tang; Lie Qian; Yuke Wang;
Global Telecommunications Conference, 2005. GLOBECOM '05. IEEE
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4. Cyclic delay diversity with bit-interleaved coded modulation in orthogon multiple access
Bauch, G.; Malik, J.S.;
Wireless Communications, IEEE Transactions on
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...one of the OFDM carriers for carrying...stored in the interleaver memory, in...comprises an interleaver memory operable...symbols for the OFDM carriers to effect...determined by an address generator, an address...comprises a linear feedback shift register including...
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2. Interleaver for mapping symbols on the carriers of an OFDM system
Peron, Jean-Luc (SONY UNITED KINGDOM LIMITED), *EUROPEAN PATENT APPLICATION*, May 2006
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...one of the OFDM carriers for carrying...stored in the interleaver memory, in...comprises an interleaver memory operable...symbols for the OFDM carriers to effect...determined by an address generator, an address...comprises a linear feedback shift register including...
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3. Interleaver for mapping symbols on the carriers of an OFDM system
Peron, Jean-Luc (SONY UNITED KINGDOM LIMITED), *EUROPEAN PATENT APPLICATION*, Sep 2004
patno:EP1463255
...one of the OFDM carriers for carrying...stored in the interleaver memory, in...comprises an interleaver memory operable...symbols for the OFDM carriers to effect...determined by an address generator, an address...comprises a linear feedback shift register including...
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4. Interleaver for mapping symbols on the carriers of an OFDM system
Peron, Jean-Luc (SONY UNITED KINGDOM LIMITED), *EUROPEAN PATENT*, Sep 2004
patno:EP1463256
...one of the OFDM carriers for carrying...stored in the interleaver memory,

in...comprises an interleaver memory operable...symbols for the OFDM carriers to effect...determined by an address generator, an address...comprises a linear feedback shift register including...
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Peron, Jean-Luc, *UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION*, Dec 2004
patno:US20040246888
...0044] An address generator 102.2 for...parts of the address generator for the 8...using a LFSR (Linear Feedback Shift Register). [0046] The...340 and an address generator 342. The interleaver memory is...
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Application Number Information

Application Number: 10/806524
Assignments

Examiner Number: 80488 / TORRES, JUAN

Filing or 371(c) Date: 03/23/2004 eDan

Group Art Unit: 2611 IFW Madras

Effective Date: 03/23/2004

Class/Subclass: 375/260.000

Application Received: 03/24/2004

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Pat. Num./Pub. Num: /20040246888

Interference Number:

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Issue Date: 00/00/0000

Unmatched Petition: NO

Date of Abandonment: 00/00/0000

L&R Code: Secrecy Code:1

Attorney Docket Number: 282575US8X

Third Level Review: NO

Secrecy Order: NO

Status: 51 /EX PARTE QUAYLE ACTION MAILED

Status Date: 12/10/2007

Confirmation Number: 2739

Oral Hearing: NO

Title of Invention: DATA PROCESSING APPARATUS AND METHOD OPERABLE TO MAP AND DE-MAP SYMBOLS AND CARRIER SIGNALS OF AN ORTHOGONAL FREQUENCY DIVISION MULTIPLEXED (OFDM) SYMBOL

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
10806524	Not Issued	51	03/23/2004	Data processing apparatus and method operable to map and de-map symbols and carrier signals of an Orthogonal Frequency Division Multiplexed (OFDM) symbol	PERON, JEAN-LUC

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